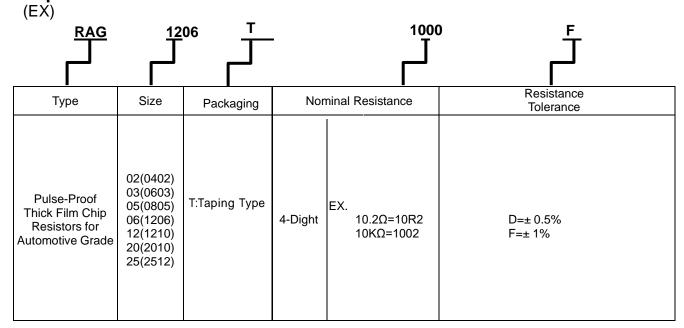


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1 Scope:

- 1.1 This specification is applicable to lead free and halogen free of ROHS directive for RAG series pulse-proof thick film chip resistors .
- 1.2 This product is for automotive electronic application.
- 1.3 AEC-Q200 qualified, grade 0.

2 Explanation Of Part Number:



3 General Specifications:

	Rated	Max.	Max.	T.C.R	Resistanc	e Range
Туре	Power at	Working Voltage	Overload Voltage	(ppm/°C)	D(±0.5%) E-96	F(±1%) E-96
RAG	1 ,,,	F0\/	100V	±200	$10\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega \! \leq \! R \! \leq \! 1M\Omega$
(0402)	1 16 W	50V	1000	±400	1Ω≦R≦10Ω	1Ω≦R≦10Ω
RAG	<u>1</u> W	75V	150V	±200	10Ω≦R≦1MΩ	10Ω≦R≦1MΩ
(0603)	<u>4</u> vv	750	1507	±400	1Ω≦R≦10Ω	1Ω≦R≦10Ω
RAG	<u>2</u> 5	4501/	2001/	±200	10Ω≦R≦1MΩ	10Ω≦R≦1MΩ
(0805)	<u>5</u> vv	150V	200V	±400	1Ω≦R≦10Ω	1Ω≦R≦10Ω
RAG	1 ,,,	200V	400\/	±200	10Ω≦R≦1MΩ	10Ω≦R≦1MΩ
(1206)	<u>1</u> W	200 V	400V	±400	1Ω≦R≦10Ω	1Ω≦R≦10Ω
RAG	3 4 W	200V	400\/	±200	10Ω≦R≦1MΩ	10Ω≦R≦1MΩ
(1210)	4 VV	2007	400V	±400	$1\Omega {\le} R {\le} 10\Omega$	1Ω≦R≦10Ω
RAG	3 4 W	200V	400V	±200	$10\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega \! \leq \! R \! \leq \! 1M\Omega$
(2010)	4 70	2007	4000	±400	1Ω≦R≦10Ω	1Ω≦R≦10Ω
RAG	410/	2001/	400\/	±200	10Ω≦R≦1MΩ	10Ω≦R≦1MΩ
(2512)	1W	100 2000 ±400	200V 400V		1Ω≦R≦10Ω	1Ω≦R≦10Ω
	Operatin	g Temperature R	Range		-55°C ~+155°C	

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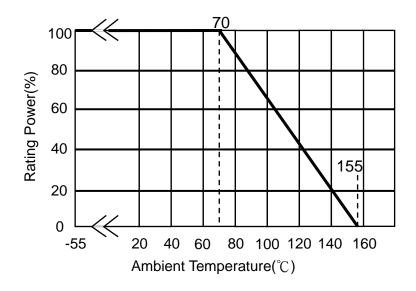


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3.1 Power Derating Curve:

Operating Temperature Range : - 55 \sim 155 $^{\circ}$ C

For resistors operated in ambient temperatures above 70° C, power rating shall be derated in accordance with figure below.



3.2 Voltage Rating:

Rated Voltage: DC voltage or AC voltage (rms) based on the rated power.

The voltage can be calculated by the following formula. If the calculated value exceeds the Max. voltage specified in the Table 3, the Max. voltage rating is set as the voltage rating.

$$E=\sqrt{R\times P}$$

E= Voltage rating (V)

P= Power rating (W)

R= Nominal resistance(Ω)

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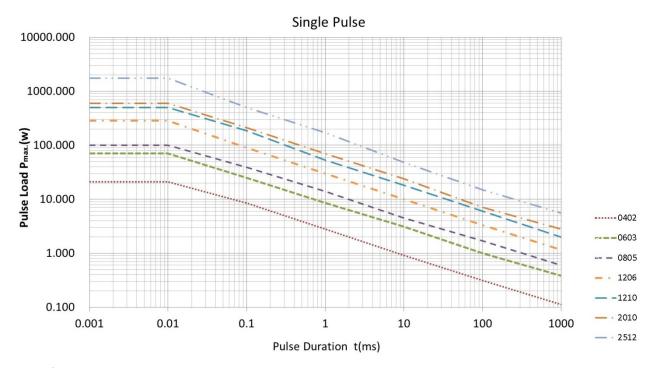


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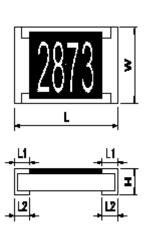
3.3 Pulse Loading Capability

Pulse on a regular basis; maximum permissible peak pulse power (Pmax) as a function of a pulse duration.

 $V_{\text{peak}} \! \leq \! 0402 (100 \text{V}) \, \cdot \, 0603 (150 \text{V}) \, \cdot \, 0805 (300 \text{V}) \, \cdot \, 1206 (400 \text{V}) \, \cdot \, 2010 (400 \text{V}) \, \cdot \, 2512 (400 \text{V})$



4 Dimensions:



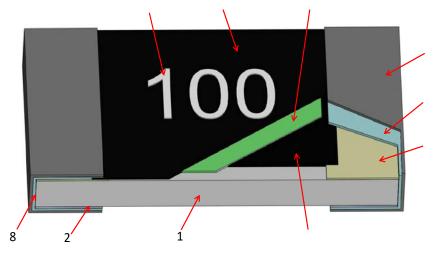
						Unit:mm
Туре	Dimension Size Code	L	W	Н	L1	L2
RAG	0402	1.00±0.10	0.50±0.05	0.30±0.05	0.20±0.10	0.25±0.10
RAG	0603	1.60±0.10	0.80±0.10	0.45±0.10	0.30±0.15	0.30±0.15
RAG	0805	2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.15
RAG	1206	3.05±0.10	1.55±0.10	0.55±0.10	0.45±0.20	0.35±0.15
RAG	1210	3.05±0.10	2.55±0.10	0.55±0.10	0.50±0.20	0.50±0.20
RAG	2010	5.00±0.20	2.50±0.20	0.55±0.10	0.60±0.20	0.60±0.20
RAG	2512	6.30±0.20	3.20±0.20	0.55±0.10	0.60±0.20	0.60±0.20

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5 Structure Graph:



1	Ceramic substrate	6	2nd Protective coating
2	Bottom inner electrode	7 Marking	
3	Top inner electrode	8	Terminal inner electrode
4	Resistive layer	9	Ni plating
5	1st Protective coating	10	Sn plating

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6 Reliability Test:

Item	Conditions	Specifications
		Resistors
	Put the specimens in the chamber with temperature of	△R=±1.0%
High	155±3℃ for 1000 hours. Then take them out to stabilize	
Temperature	in room temperature for 24±4hr or more, and measure of	
Exposure (Storage)	its resistance variance rate.	
(Storage)	Experiment evidence: AEC-Q200	
	Put the specimens in the High & low temperature test	△R=±1.0%
	chamber with temperature varies from -55°C to 125°C	21.070
_	for 15 minutes and total 1000 cycles. Take them out to	
Temperature	stabilize in room temperature for 24±4hr or more, and	
Cycling	measure of its resistance variance rate.	
	Experiment evidence: AEC-Q200	
	Applied 2.5 times rated voltage for 5 seconds and	△ R=±1.0%
Short Time	release the load for about 30 minutes, then measure its	
Overload	resistance variance rate. (Rated voltage refer to item 3.	
	general specifications)	
	Refer to JIS-C5201-1 4.13 Solder the specimens on the test PCB and put them into	△ R=±2.0%
	the constant temperature humidity chamber with 85±2°C	△ K=±2.0%
	and 85±5%RH. Then apply the test voltage that	
	calculates based on the 10% of rated power for 1000hrs.	
Biased Humidity	Then take them out to stabilize in room temperature for	
	24±4hr or more, and measure of its resistance variance	
	rate.	
	Experiment evidence: AEC-Q200	
	Solder the specimens on the test PCB and put them in	△R=±2.0%
	the chamber with temperature of 125±3℃ and load the	
	voltage for 1000 hours. Then take them out to stabilize in	
Operational	room temperature for 24±4hr or more, and measure of its	
Life	resistance variance rate. Note: The input voltage shall refer to the power de-rating	
	curve (referring to page 2,No.3.1)	
	learve (referring to page 2,140.5.1)	
	Experiment evidence: AEC-Q200	
	The specimens are fully immersed into the Pb-free solder	△R%=±1.0%
	pot, then take them out to stabilize for 1 hour or more and	
Resistance to	measure of its resistance variance rate.	
Soldering Heat		
	Soldering duration: 10±1sec.	
	Experiment evidence AEC-Q200	
	Put the specimens on the test fixture and two	△R=±3.0%
	(2) discharges (3KVDC) shall be applied to each PUT,	
	one (1) with a positive polarity and one (1) with a	
ESD	negative polarity. Afterwards, the specimens stabilize for 30min or more and measure of its resistance variance	
LOD	rate. The test is performed with direct contact and regular	
	discharge mode. The resistor and capacitor used on the	
	spearhead is 2000Ω and $150pF$ respectively.	
	Experiment evidence AEC-Q200	

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Itom	Conditions	Specifications
Item	Conditions	Resistors
Solderability	Test method: Test item 1 (solder pot test): Method B Precondition: The specimens are subjected to 155°C dry bake for 4hrs±15min. The specimens are immersed into the flux first, then fully immersed into the solder pot, at a temperature of 235±5°C for 5+0/-0.5 sec. Then rinse with water and observe the soldering coverage under the microscope. Test item 2 (Leaching test): Method D The specimens are immersed into the flux first, then fully immersed into the solder pot, at a temperature of 260±5°C for 30+0/-0.5 sec. Then rinse with water and observe the soldering coverage under the microscope.	1.Soldering coverage over 95% 2.At the edge of terminal, the object underneath (e.g. white ceramic) shall not expose.
Electrical Characterization	Experiment evidence AEC-Q200 $TCR(ppm/^{\circ}) = \frac{(R2-R1)}{R1(T2-T1)} \times 10^{6}$ R1: Resistance at room temperature (Ω) R2: Resistance at -55° \mathbb{C} or +125° \mathbb{C} (Ω) T1: Room temperature (° \mathbb{C}) T2: Temperature -55° \mathbb{C} or +125° \mathbb{C} . Experiment evidence: AEC-Q200	Refer to item 3. general specifications
Board Flex (Bending Test)	Solder the specimens on the test PCB and put the PCBA onto the Bending Tester. Add force at the central part of PCB, and the duration of the applied forces shall be 60 (+ 5) Sec. Measure of its resistance variance rate in load. Bending depth (D) RAG0402 0603 0805=5mm RAG1206 1210=3mm RAG2010 2512=2mm Experiment evidence: AEC-Q200	△R=±1.0% No mechanical damage, peel-off of side end or chip crack.

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7 Measurement Point:

Measure from bottom electrodes			Unit : mm
Α Α	TYPE DIM	Α	В
	RAG0402	0.80±0.05	0.24±0.05
	RAG0603	1.35±0.05	0.35±0.05
	RAG0805	1.80±0.05	0.35±0.05
	RAG1206	2.90±0.05	0.35±0.05
Current Terminal	RAG1210	2.90±0.05	0.35±0.05
	RAG2010	4.50±0.05	1.15±0.05
Voltage Terminal	RAG2512	5.90±0.05	1.60±0.05

8 Plating Thickness:

- **8.1 Ni**:**≧2** *μ* m
- 8.2 Sn(Tin): \ge 3 μ m
- 8.3 Sn(Tin):Matte Sn

9 Rule of package empty quantity:

- 9.1 Empty quantity for each reel is not allowed to exceed 0.1% of the whole quantity,
- 9.2 and continuous 2pcs (included) empty are also unallowed.

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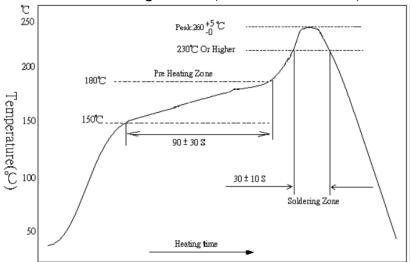


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10 Technical application notes: (This is for recommendation, please customer perform adjustment according to actual application)

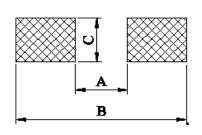
- 10.1 Recommend Soldering Method
 - 10.1.1Lead Free IR Reflow Soldering Profile (MEET J-STD-020D)



Remark: The peak temperature of soldering heat is 260 +5/-0 $^{\circ}$ C for 10 seconds. 10.1.2Soldering Iron: temperature 350 $^{\circ}$ C ±10 $^{\circ}$ C, dwell time shall be less than 3 sec.

10.2 Recommend Land Pattern Design (For Reflow Soldering)

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.



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TYPE	А	В	С
RAG0402	0.5	1.5	0.6
RAG0603	0.8	2.1	0.9
RAG0805	1.2	3.0	1.3
RAG1206	2.2	4.2	1.6
RAG1210	2.2	4.2	2.8
RAG2010	3.5	6.1	2.8
RAG2512	3.8	8.0	3.5

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RAG Series Pulse-Proof Thick Film Chip Resistors Product Specification (Automotive Grade)

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10.3 Automobile Electronic Application:

This specification is for automobile electronic use. RALEC will take no responsibility if any damage, cost or loss occurs when the product has been used in any special circumstances.

- (a) Information , entertainment , navigation , audio control units.
- (b) Comfortable door, window, seat control unit.
- (c) Internal lighting control unit.

10.4 Environment Precautions:

If consumer intends to use our company product in special environment or condition (including but not limited to those mentioned below), then will need to make individual recognition of product features and reliability accordingly.

- (a) Used in high temperature and humidity environment
- (b) Exposed to sea breeze or other corrosive gas, such as Cl2 · H2S · NH3 · SO2 and NO2.
- (c) Used in non-verified liquids including water, oil, chemical and organic solvents.
- (d) Using non-verified resin or other coating material to seal or coat our Company product.
- (e) After soldering, it is necessary to use water-soluble detergents to clean residual solder fluxes, even though no-clean fluxes are recommended.

10.5 Momentary Overload Precautions:

The product might be out of function when momentary overloaded. Please make sure to avoid momentary overloading while using and preserving.

10.6 Operation and Processing Precautions:

- (a) Avoid damage to the edge of resistor and protective layer caused by mechanical stress.
- (b) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.
- (c) Make sure the power rating is under the limit when using the resistor. When power rating is over the limit, the resister will be overloaded. There might be machinery damage due to the climbing temperature
- (d) If the resister will be exposed under massive impact load (shock wave) in a short period of time, the working environment must be set up well before use.
- (e) Please make evaluation and confirmation when the product is well used in your company and have a through consideration of its fail-safe design to ensure the system safety.

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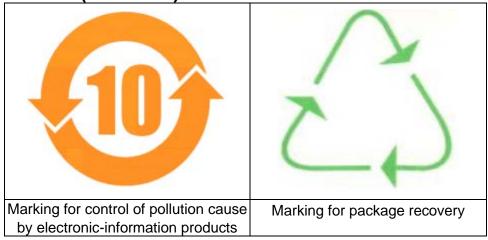
RAG Series Pulse-Proof Thick Film Chip Resistors Product Specification (Automotive Grade)

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11 Storage and transportation requirement:

- 11.1 The temperature condition must be controlled as 25±5°C, and the R.H. must be controlled as 60±15%. The stock can maintain quality level in two years.
- 11.2 Please avoid the mentioned harsh environment below when storing to ensure product performance and its' weldability. Places exposed to sea breeze or other corrosive gas, such as Cl₂ × H₂S × NH₃ × SO₂ and NO₂.
- 11.3 When the product is moved and stored, please ensure the correct orientation of the box. Do not drop or squeeze the box. Otherwise, the electrode or the body of the product may be damaged.

12 The carton for electronic-information products is made by the symbol as follows: (For China)



13 Attachments:

13.1 Document Revise Record (QA-QR-027)

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